

SSEC - NATIONAL CONFERENCE ON**INTELLIGENT AND EFFICIENT ELECTRICAL SYSTEMS*****NCIEES'10* - 5th March 2010**

S.NO	NAME	COLLEGE	DETAILS
1	Adaptive edge detection for real time image processing using FPGA	Sri Muthukumaran Institute of Technology	G. Hemalatha hema_19_9@yahoo.co.in
2	Implementation of regenerative snubber based novel high frequency link inverter	Adhiparasakthi Engineering College	P.Muthu, PG scholar. P.Radika, M.E., (Ph.D), Professor,EEE muthuvit2007@gmail.com
3	Self adaptable ZCS for current fed Full bridge DC/DC converter	S.V.C.E, sriperumbudur	K. Nanda Kumar PG student nandanam2004@yahoo.co.in.
4	Video quality prediction for MPEG-4 video streaming	Sri Krishna Engineering College, panapakkam	Rajila rajan, PG Student, K. Jayavelu, Asst. Prof, ECE rozraji@gmail.com
5	Tetware for realtime and embedded systems	Dr.RR Dr.SR Engineering College, avadi.	V. Kavitha, Lecturer vkavitha25@gmail.com
6	Harmonic reduction in three phase PWM rectifier using FPGA based controller	Mepco Schlenk Engineering College, sivakasi	E. Divya, PG student J. Gnanavadivel Asst. Prof divya.ellappan@gmail.com g_vadivel@rediff.com

7	Micro controller based soft switch mode rectifier	College of Engineering, guindy.	S.Sivakumar, PG scholar Dr. S.Chandramohan, Ph.D, Asst. Prof., DEEE siva_kumar_sp@yahoo.co.in
8	Line loss minimization and voltage regulation using UPFC	Velammal Engineering College.	Thomas John Herethomas@gmail.com
9	Simulation and implementation of higher order current mode controlled converter for sustaining slow scale oscillation	Rajalakshmi Engineering College	T.Shobana, PG student V. Devi Maheswaran, Asst. Prof, EEE shobi_123@yahoo.co.in
10	Luo converter for parallel connected solar photovoltaic system	KSR College of Technology, Tiruchengode.	V.Prabhu, PG student S.Gomathi, Professor prabhuveeramani@yahoo.co.in
11	Campus security wireless sensor network	Nagpur University	Piyush Babre, Anup Kolhe, Aakash Awasthi piyushbabre@yahoo.co.in, kolheanup@yahoo.in, aakashawasthi293@gmail.com
12	FPGA controlled simplified h-bridge multilevel inverter	S.V.C.E, sriperumbudur.	P. Suresh, PG student T. Annamalai, Sr. lecturer sureshvit@yhoo.co.inannamalai@svce.ac.in,

13	Modelling and simulation of microturbine in distributed generation system		Prakash.M Abul Hassan.S prakash13688@gmail.com abulhassan1987@gmail.com
14	Digital simulation of grid interactive wind energy conversion	Velammal Engineering College	C.S.Gajalakshmi,PG scholar Dr.P. Chidambaram anushrithik@gmail.com drpchida@live.com
15	A new reduction method for high order Discrete -time systems via particle swarm optimization	Andhra University, visakhapatanam-530003	Dr.G.V.K.R.Sastry, G.Vasu, I.Annapurna profsastrygvkr@yahoo.com
16	Improved Routh Approximation For High Order Discrete -Time Systems Reduction Via W-Domain	Andhra University, visakhapatanam-530003	Dr.G.V.K.R.Sastry, G.Vasu, I.Annapurna profsastrygvkr@yahoo.com
17	Remote Monitoring Of Patient Parameters Using Body Area Networks	Amrita School of Engineering, Bangalore	Ashwini Mary Job, Deepika Padiyar, Kishore Sheik Ahamed, Vaishak Venket, K. Deepa
18	Quasi Current Mode Control For Phase Shifted Series Resonant Converter	Rajalakshmi Engineering College	J.Manochithra Yadav, PG student, R.N.Ponnalagu, Asst. Prof., EEE

19	Analysis Of 3 ϕ PWM Boost Rectifier To Regulate Output Voltage And Compensate Source Current Under Balanced Supply Using Sliding Mode Controller	College Of Engineering, guindy	P.Shanthi, PG scholar, Dr.G.Uma, Asst.prof.
20	Optimal Tuning Of Pi Controller Using Genetic Algorithm For Induction Motor Speed Control	Rmk Engineering College	G.Shalini, PGstudent, Dr.Gita Ramadas, prof., Dr.S.Sridar Rao, prof., Dr.T.Thyagarajan, professor, MIET, chrompet
21	Noval techniques for compensating negative sequence voltage	ACCET Karaikudi.	Rm.Meenakshi Sundaram, ME student
22	Design and analysis of a generic control loop feedback mechanism for a four tank system		R.Jeyanthi, K.Deepa